update 10/717,343

| Ref<br># | Hits  | Search Query                                   | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|-------|--|---|---------------------|---------|------------------|
| L1       | 76774 | (326/38-41).CCLS. or ("365").CLAS.             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/10/31 08:46 |
| L2       | 1110  | 1 and (cross-coupled near inverters)           | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:46 |
| L3       | 1252  | 1 and (cross near coupled near inverters)      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:47 |
| L4       | 1252  | 2 or 3   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:47 |
| L5       | 205   | 4 and bitline                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:47 |
| L6       | 95    | 4 and bit-line                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:47 |
| L7       | 833   | 4 and (bit near line)                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:48 |
| L8       | 906   | 5 or 6 or 7                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:48 |
| L9       | 0     | 8 and (configurable near logic near resources) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:53 |
| L10      | 0     | 8 and (programmable near logic near resources) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/10/31 08:49 |

| L11 | 0  | 8 and (configuration near bit near terminal) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;            | OR | ON | 2005/10/31 08:50 |
|-----|----|--|---|----|----|------------------|
| L12 | 0  | 8 and (programming near bit near terminal)   | IBM_TDB  US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB       | OR | ON | 2005/10/31 08:51 |
| L13 | 0  | 8 and (configuration near terminal)          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:50 |
| L14 | 0  | 8 and (programming near terminal)            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:51 |
| L15 | 1  | 8 and (configurable near resources)          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:52 |
| L16 | 2  | 8 and (programmable near resources)          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:52 |
| L17 | 49 | (configurable near logic near resources)     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:54 |
| L18 | 0  | 4 and 17                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/31 08:53 |

Interference Search 10/717,343

| Ref<br># | Hits | Search Query  | DBs      | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|---|----------|---------------------|---------|------------------|
| L19      | 0    | ((memory near cells) and (memory near transistor) and (programmable near interconnection)).clm. | US-PGPUB | OR                  | ON      | 2005/10/31 09:36 |
| L20      | 1    | ((memory near cells) and (programmable near interconnection)). clm.                             | US-PGPUB | OR                  | ON      | 2005/10/31 09:35 |
| L21      | 619  | ((memory near cells) and (memory near transistor)).clm.   | US-PGPUB | OR                  | ON      | 2005/10/31 09:36 |
| L22      | 31   | ((memory near cells) and bitline and (memory near transistor)). clm.                            | US-PGPUB | OR                  | ON      | 2005/10/31 09:37 |
| L23      | 51   | ((memory near cells) and (cross-coupled near inverters)).clm.                                   | US-PGPUB | OR                  | ON      | 2005/10/31 09:38 |
| L24      | 7    | ((memory near cells) and (cross-coupled near inverters) and bitline).clm.                       | US-PGPUB | OR                  | ON      | 2005/10/31 09:41 |
| L25      | 1    | (configurable near logic near resources).clm.   | US-PGPUB | OR                  | ON      | 2005/10/31 09:42 |